

TITLE: FLASH MEMORY WITH BURIED BIT LINES INVENTOR(S): Richard M. Fastow, Chi Chang, and Sheung Hee Park

Attorney Docket #: SPSN-H0563



Replacement Sheet



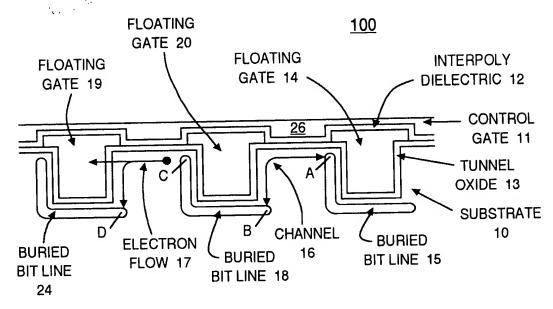


Fig. 1

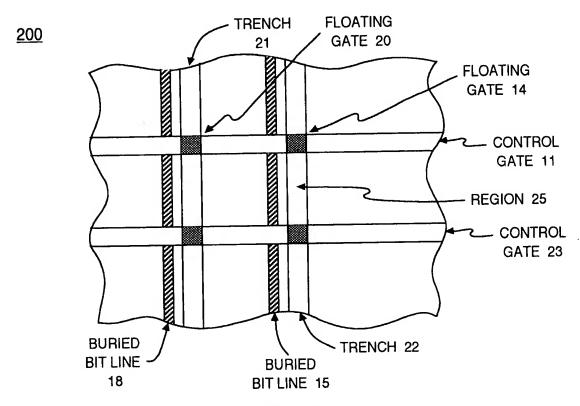


Fig. 2

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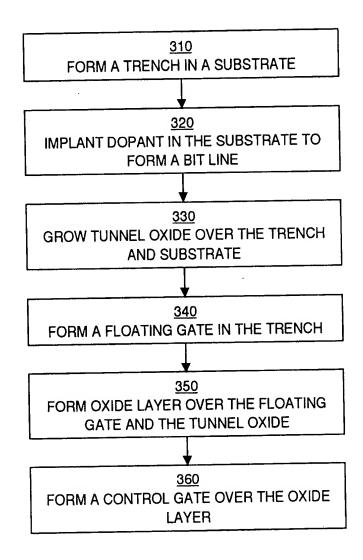


Fig. 3

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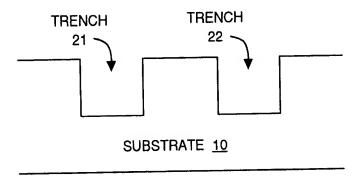


Fig. 4A

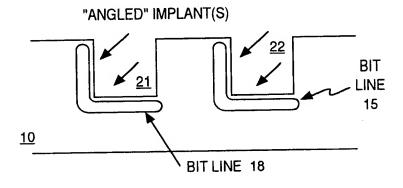


Fig. 4B

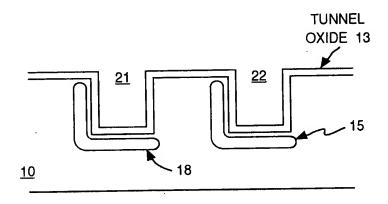


Fig. 4C

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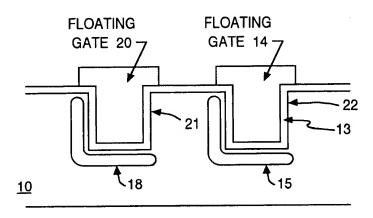


Fig. 4D

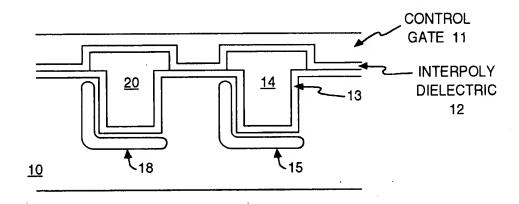


Fig. 4E